

2811 #5/103
N. White
PATENT
TESSERA 3.0, 078 DIV
-- 9/13/99

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of
Joseph Fjelstad

Application No. 09/020,647

Filed: February 9, 1998

For: SEMICONDUCTOR CHIP PACKAGE
WITH FAN-IN LEADS



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Date: July 22, 1999

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Assistant Commissioner for Patents
Washington, D.C. 20231

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

No fee is due at this time. In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on July 22, 1999.

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